

AMENDMENTS TO THE SPECIFICATION

Please replace the second full paragraph on page 5 with the following amended paragraph:

This power controlling unit of FIG. 3 comprises an AGC 31 flattening the level of downlink signals received from a neighboring BTS; a channel estimator ~~2332~~ detecting magnitude and/or phase of pilot channel of output signals from the AGC 21; a speed estimator 33 estimating a moving speed of a MS based on the detected magnitude and phase of pilot channel; a step adjustor 34 adjusting a power controlling step size (ΔP) based on the estimated moving speed; a demodulator 35 extracting TPC bits from the level-flattened downlink signals from the AGC 31; a TPC verifier 36 measuring how much reliable the extracted TPC bits are; a power level controller 37 outputting a power control signal commanding to increment or decrement current transmitting power level by the adjusted step size (ΔP) according to the TPC bits whose reliability is measured by the TPC verifier 36; and a HPA 39 power-amplifying uplink signals, which has been modulated through a modulator 38, according to the power control signal.